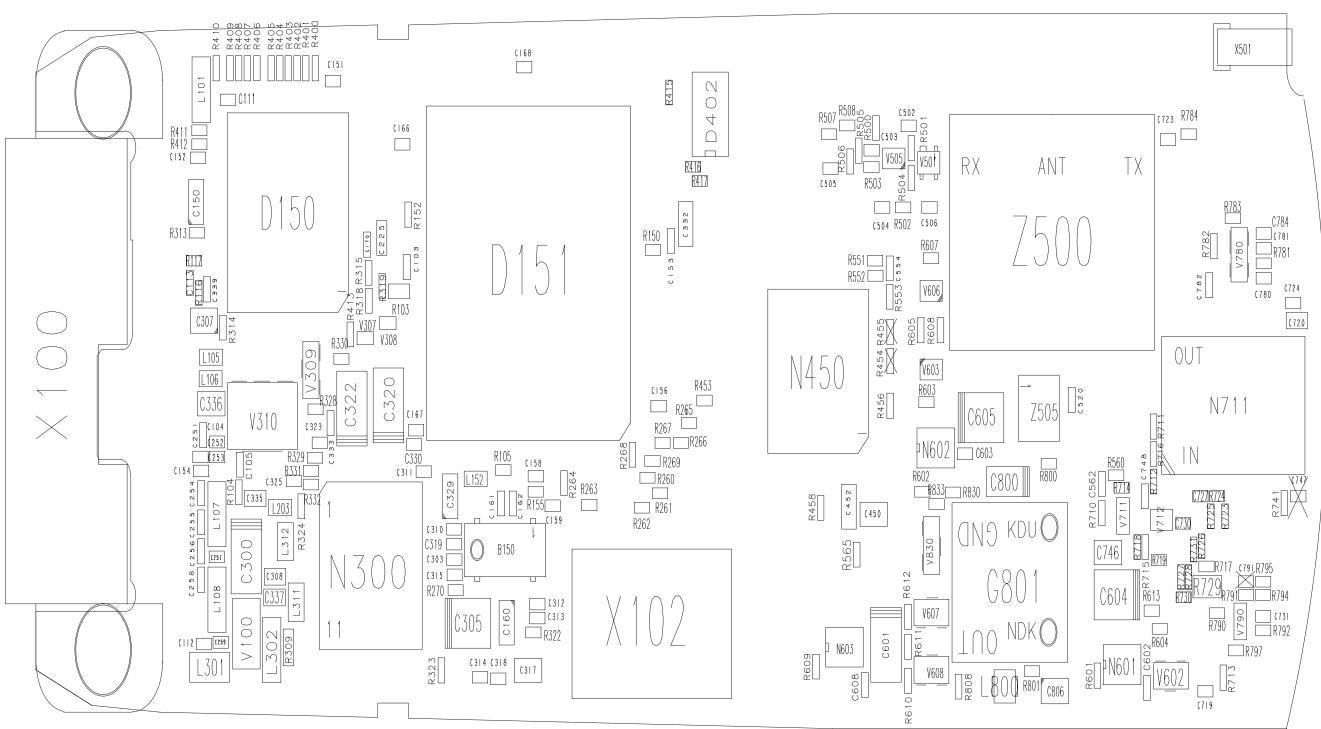
Figure 9 Component Layout Diagram Top

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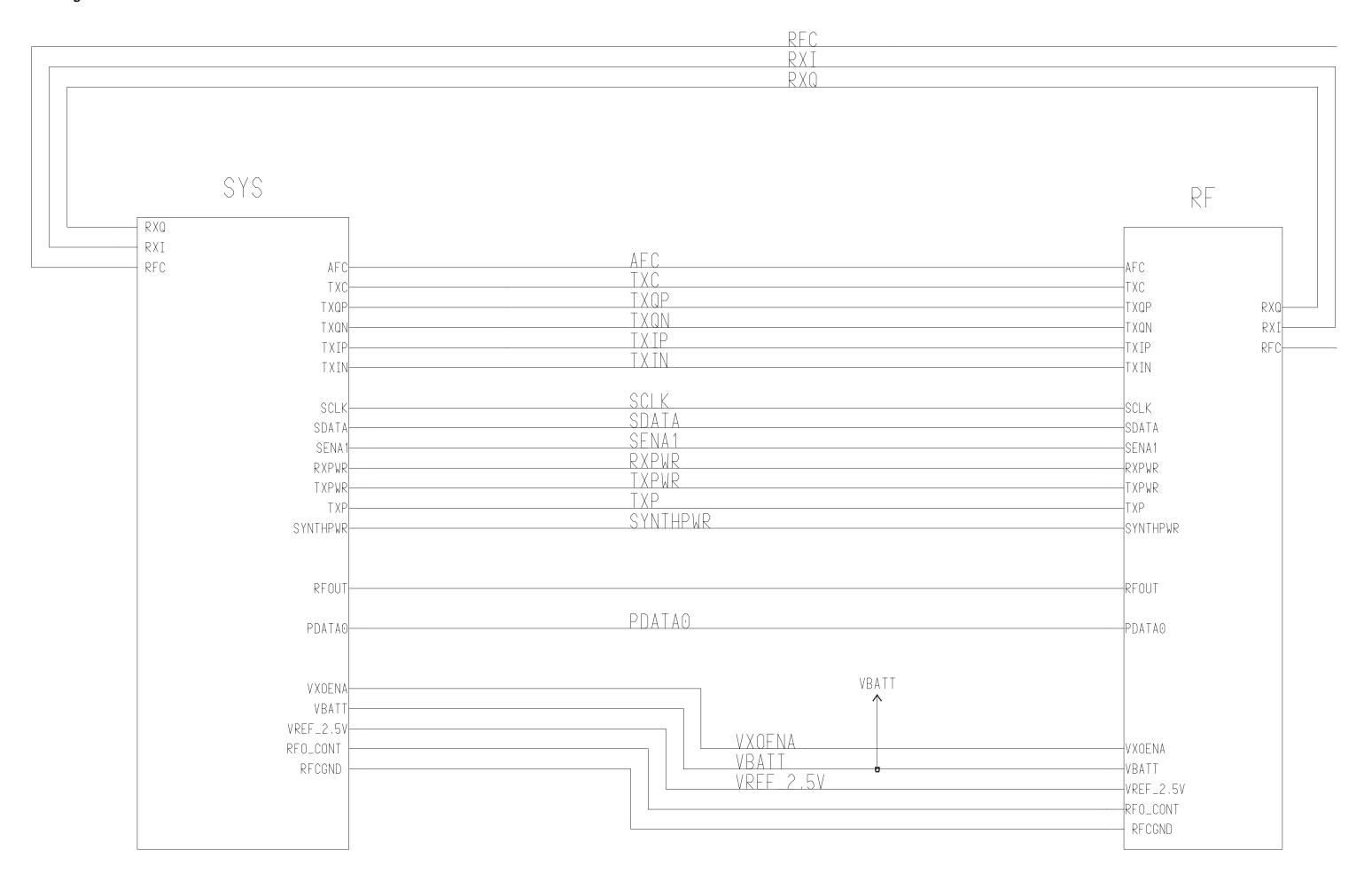


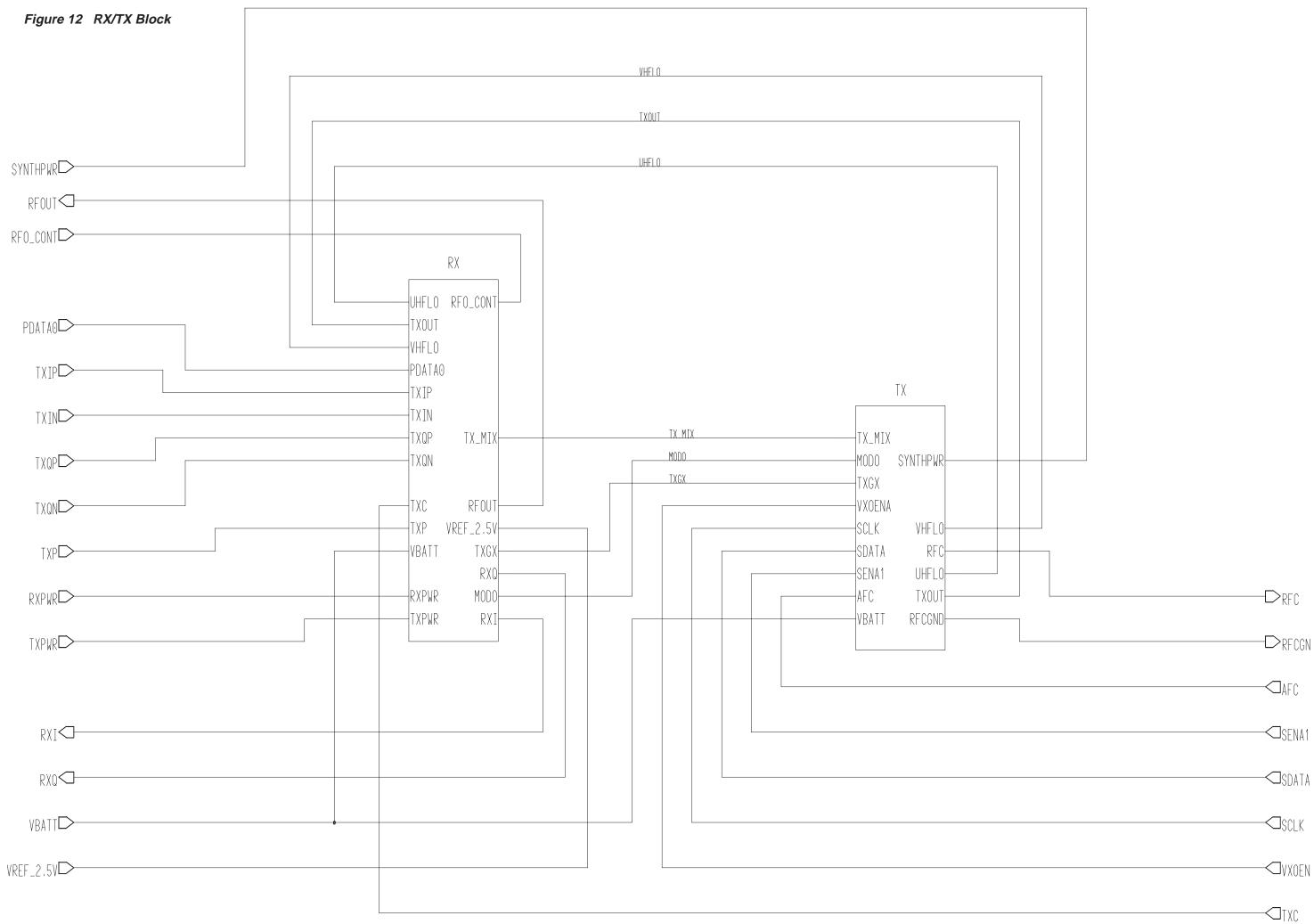
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Figure 11 SYSTEM Block





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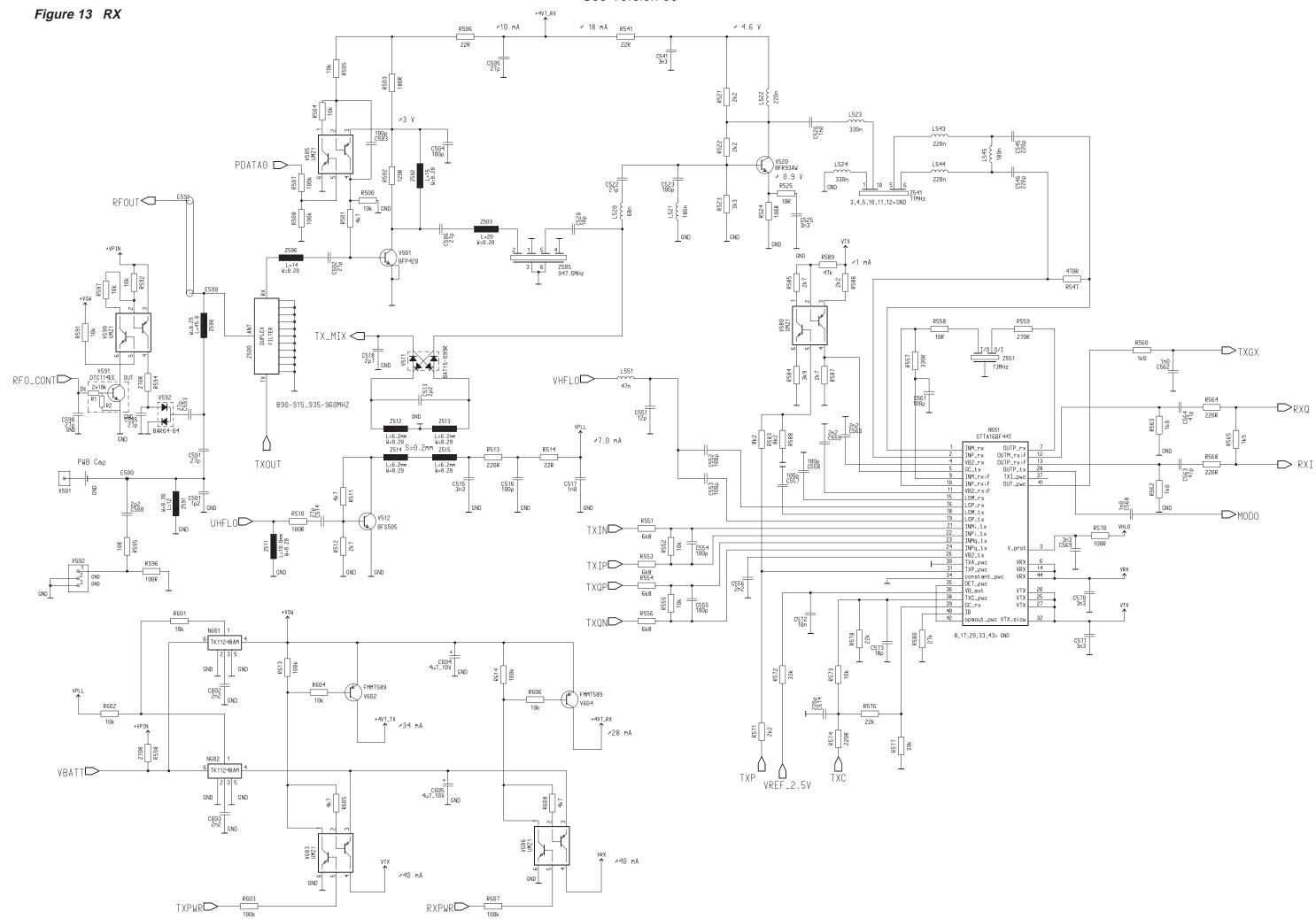
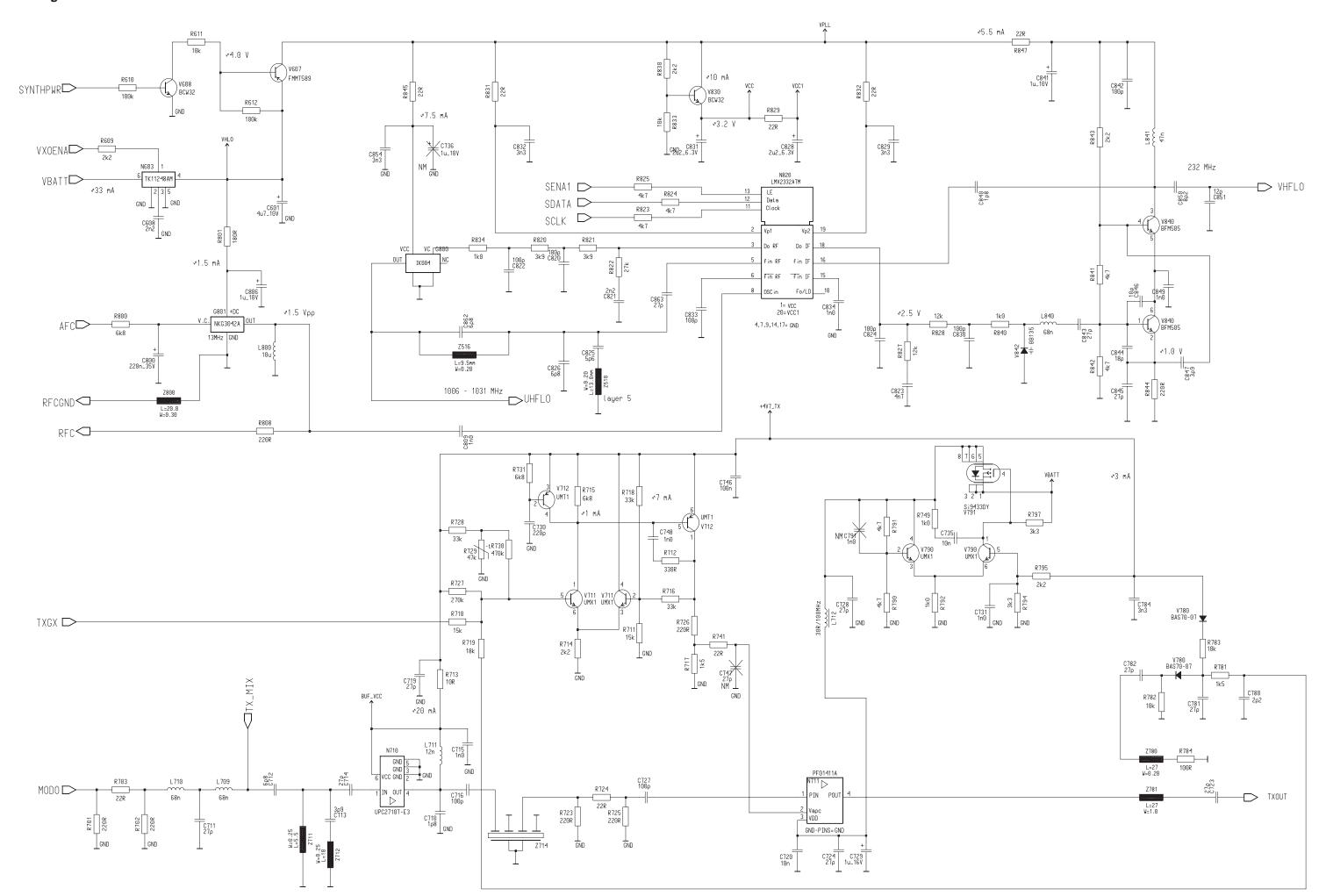
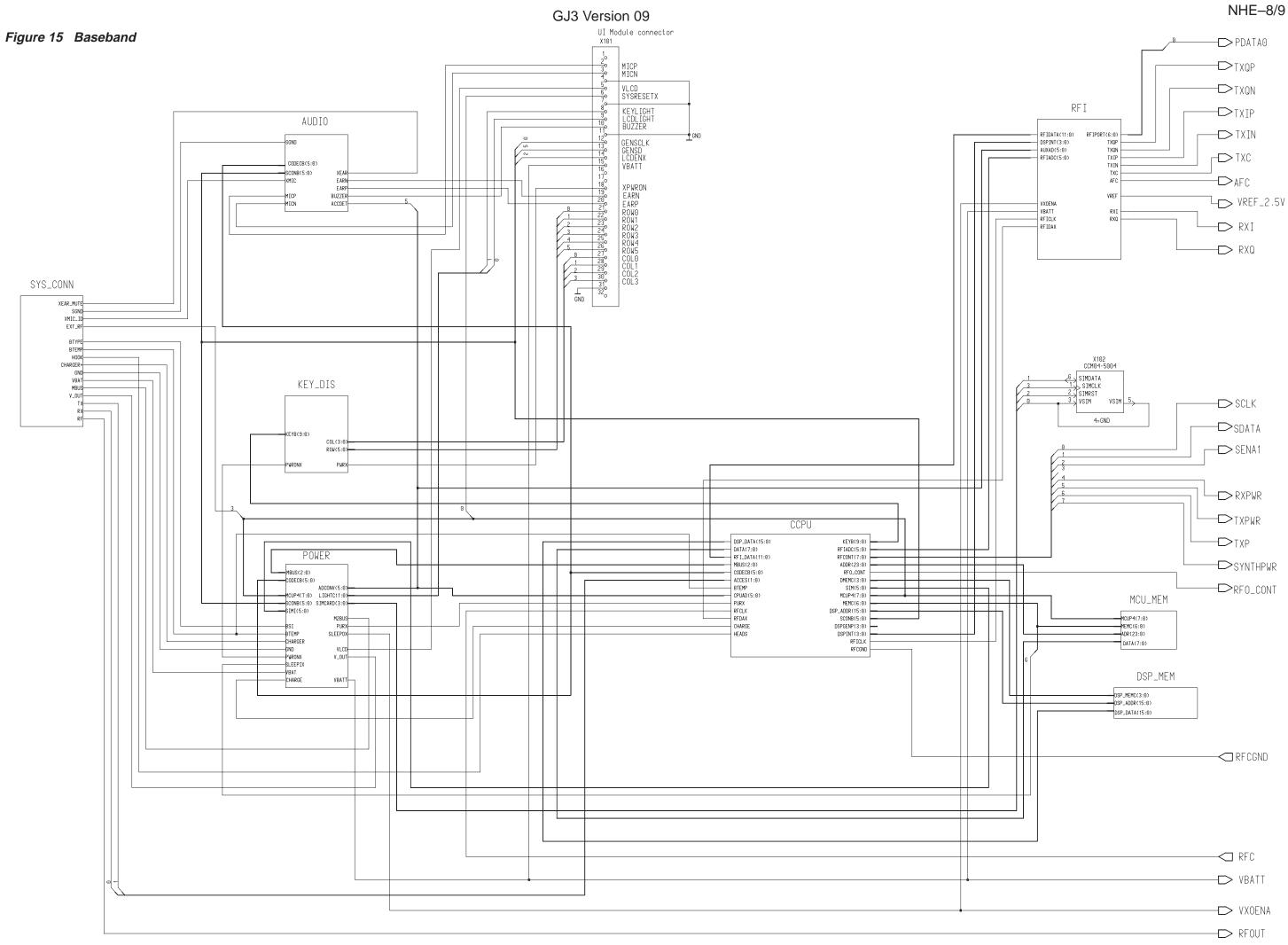
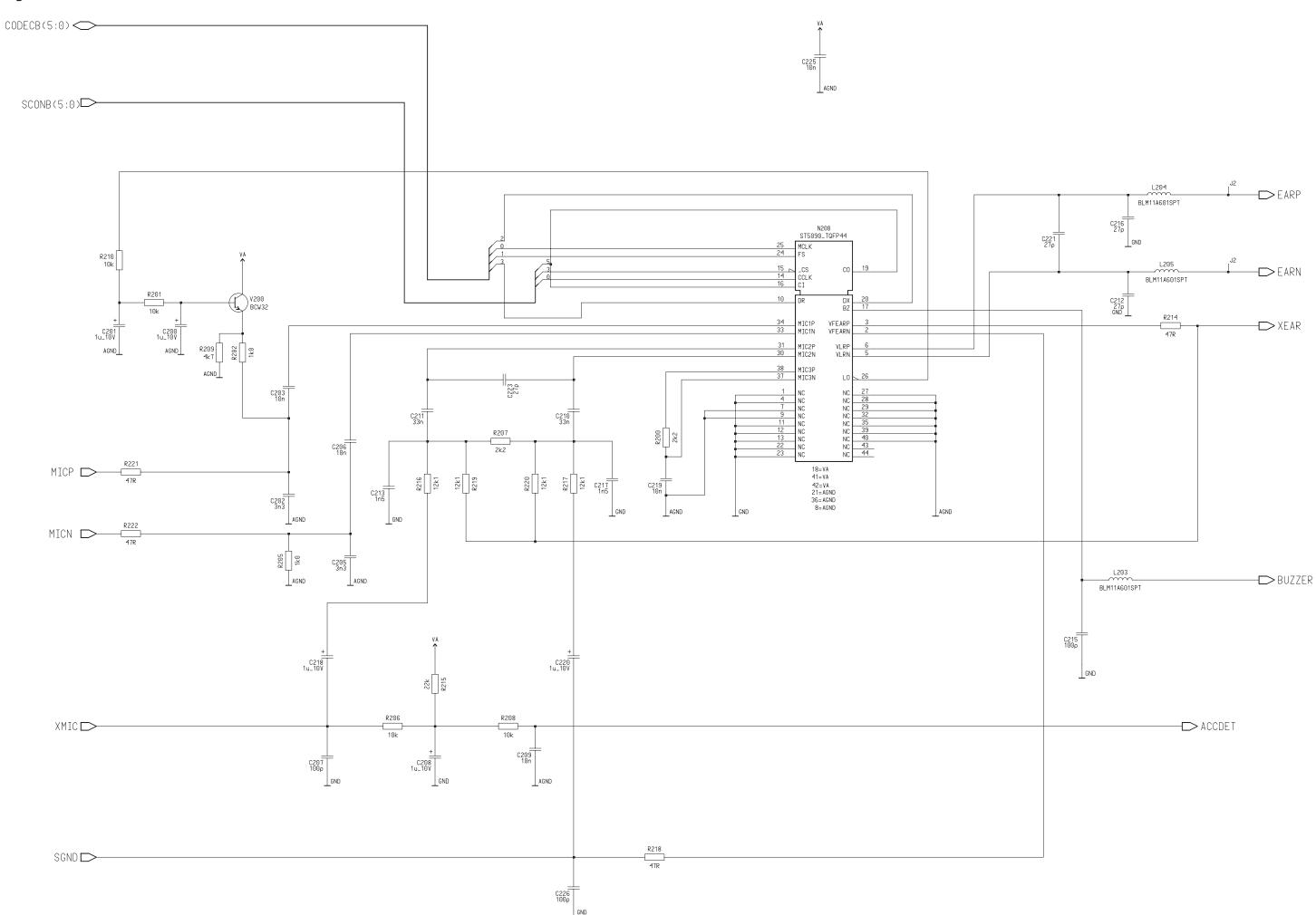


Figure 14 TX







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Figure 17 CPU

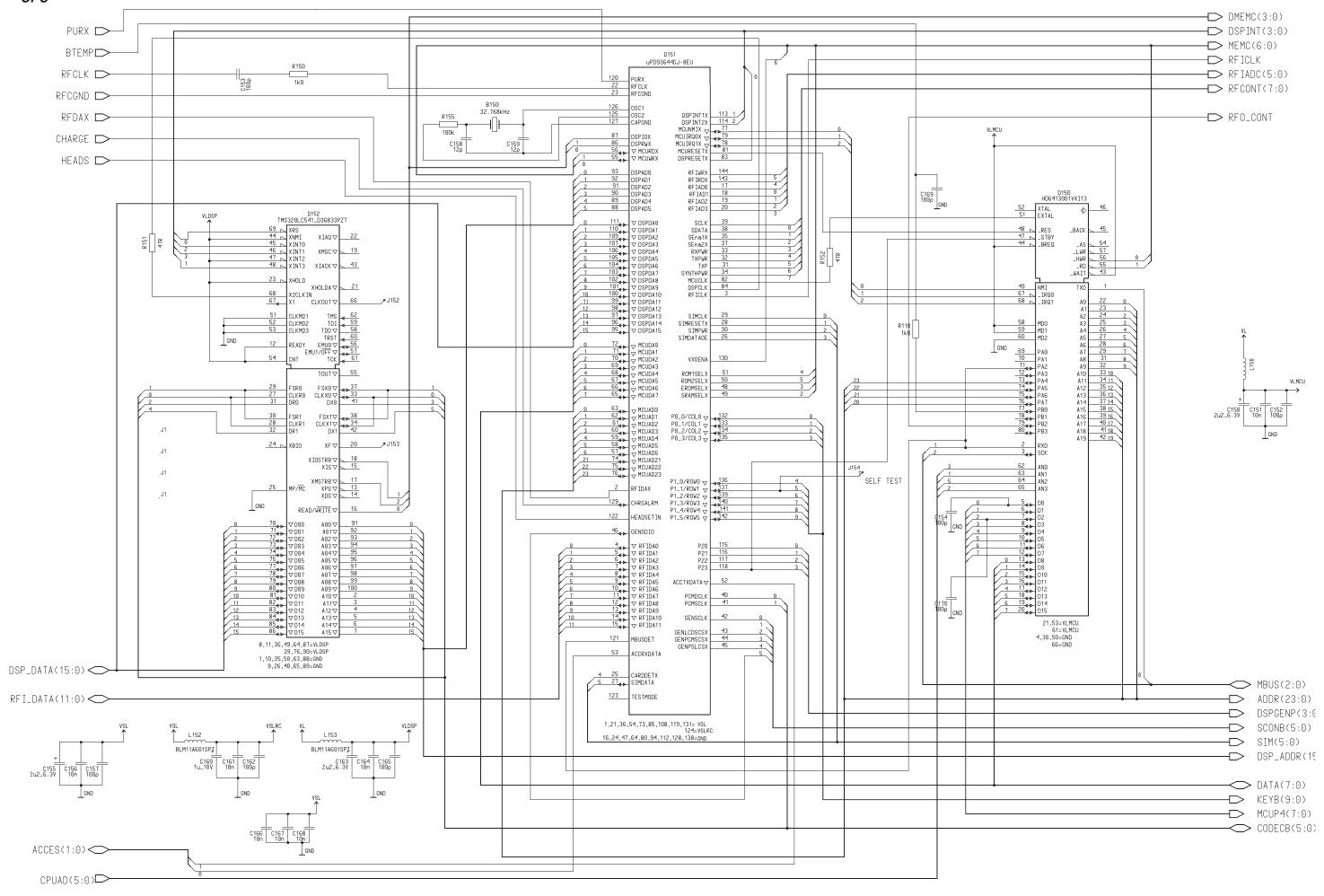
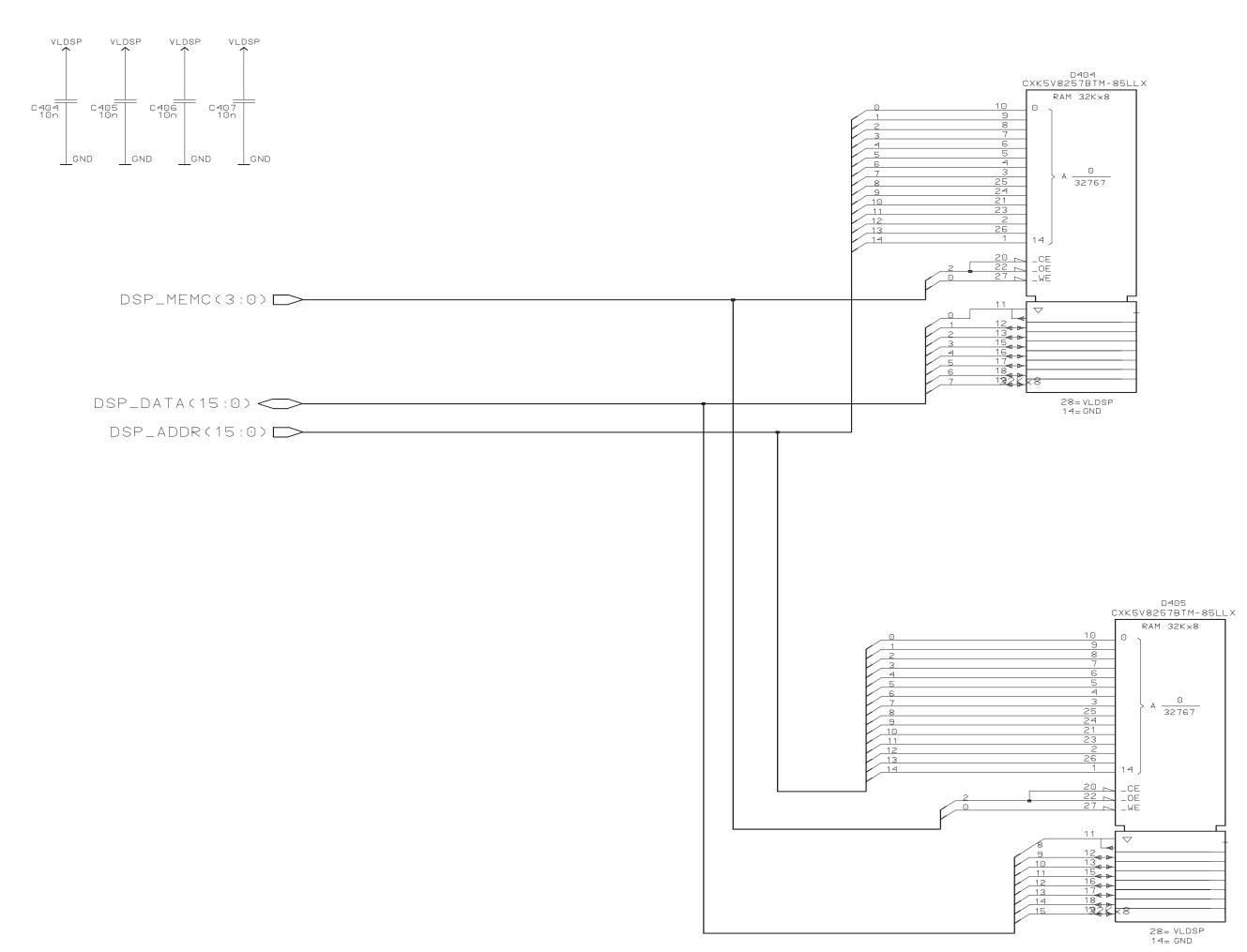
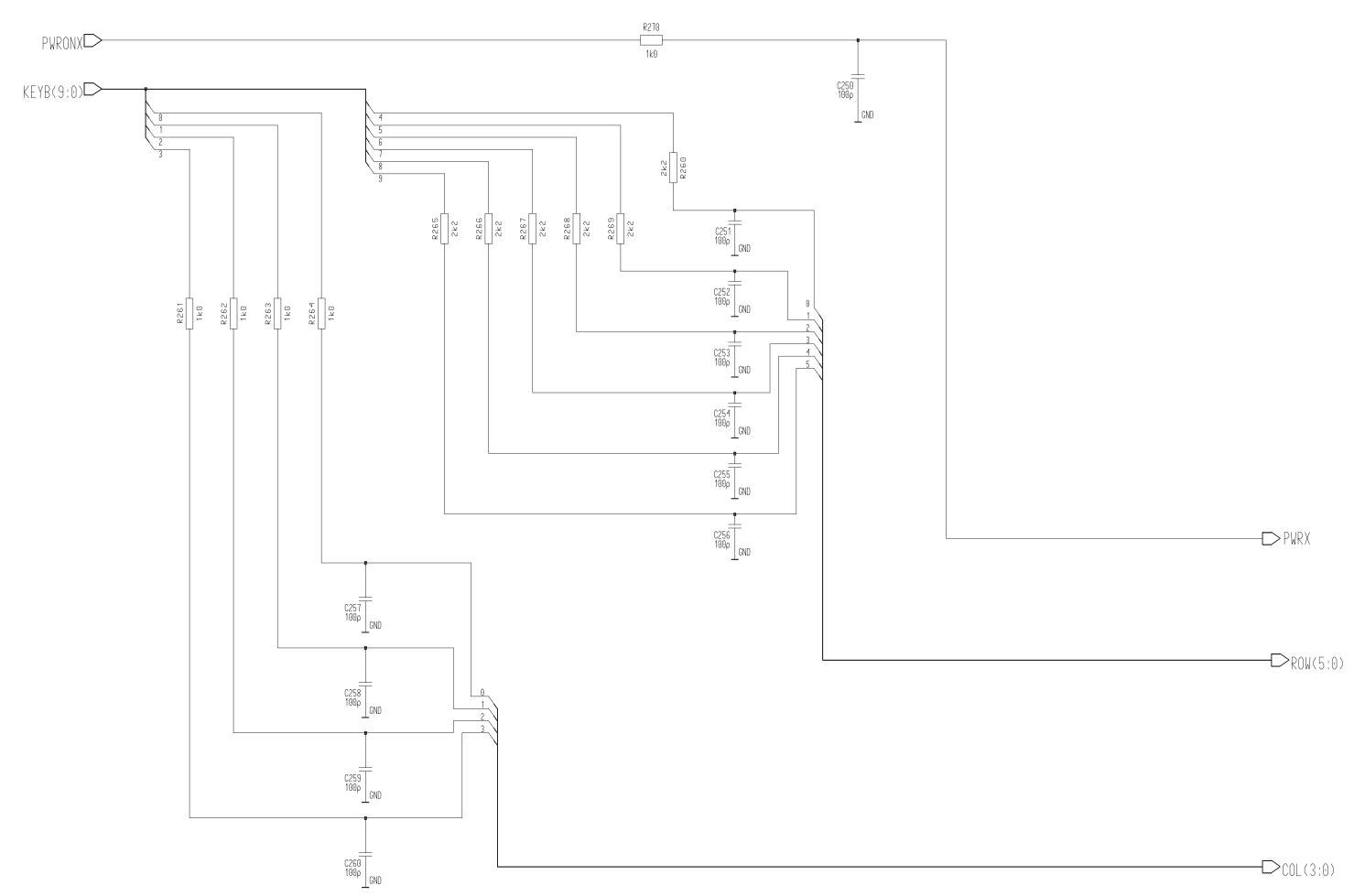


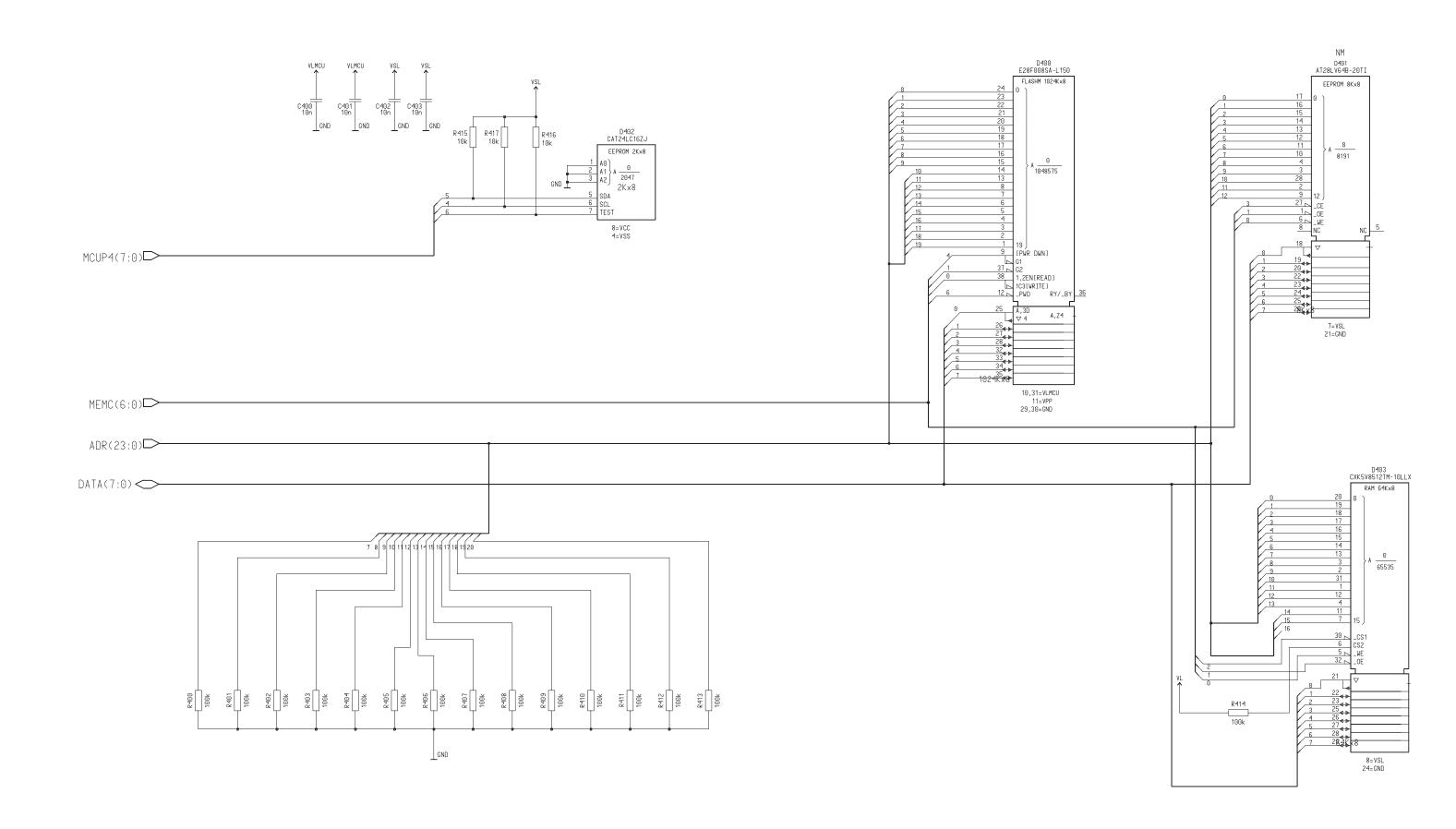
Figure 18 DSP Memory Blocks



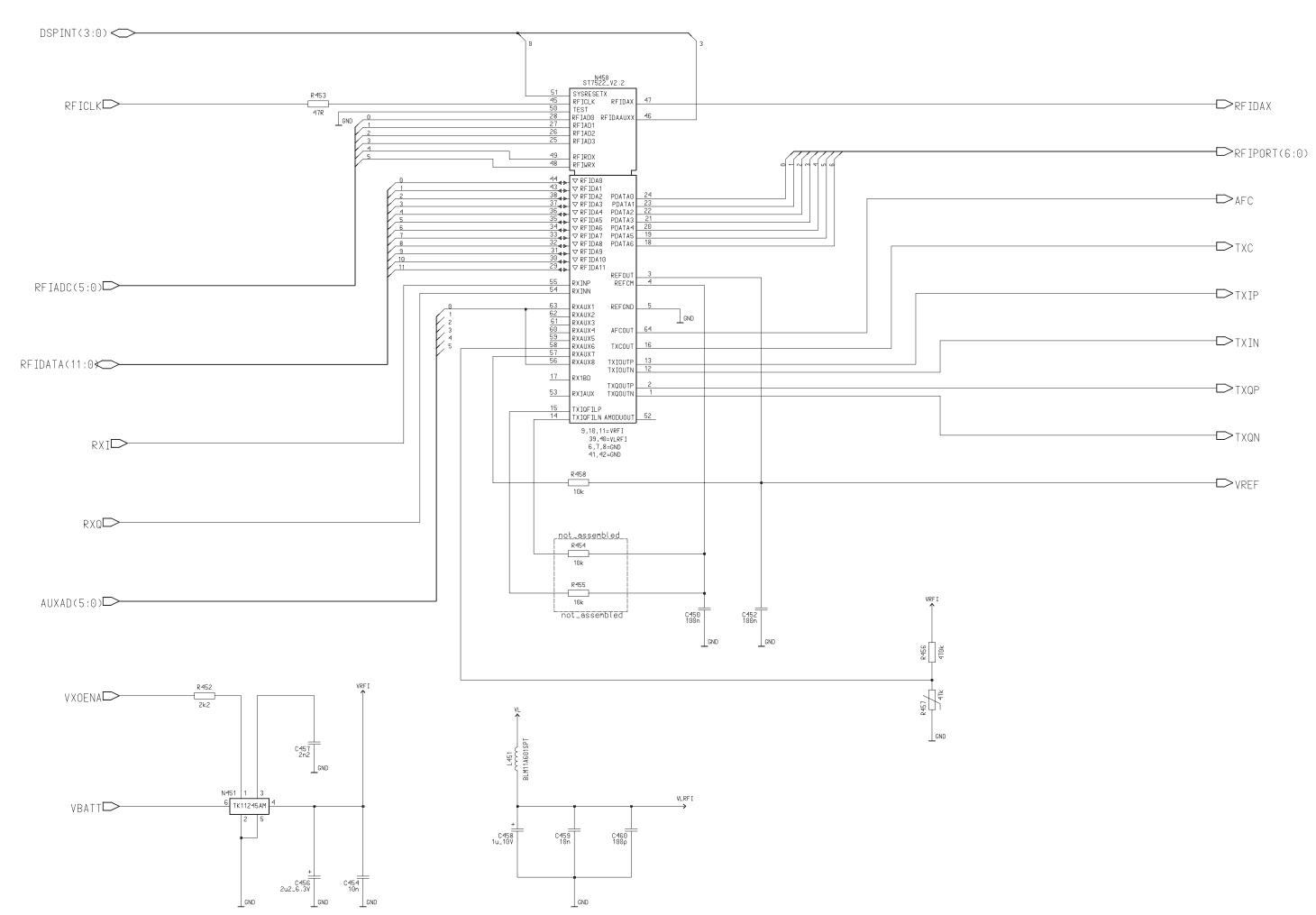


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Figure 20 MCU memory Block



MCUP4(7:0) →



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Figure 23 System Connector

